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# INFORMATION DISCLOSURE CITATION IN AN APPLICATION

ATTY. DOCKET NO.  
**43876-144**

SERIAL NO.  
**10/616,303**

APPLICANT  
**Craig HANSEN, et al.**

FILING DATE  
**July 10, 2003**

GROUP  
**2483 2676**

(PTO-1449)

## U.S. PATENT DOCUMENTS

EXAMINER'S INITIALS	CITE NO.	Document Number Number-Kind Code <sup>2</sup> (if known)	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
MM		US 4,785,393	11/15/1988	Chu et al.	
		US 4,814,976	03/21/1989	Craig C. Hansen, et al.	
		US 5,031,135	07/09/1991	Patel	
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		US 6,041,404	03/21/2000	Patrice Roussel, et al.	
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EXAMINER'S INITIALS	CITE NO.	Foreign Patent Document Country Codes -Number - Kind Codes (if known)	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines Where Relevant Figures Appear	Translation	
						Yes	No

## OTHER ART (Including Author, Title, Date, Pertinent Pages, Etc.)

EXAMINER'S INITIALS	CITE NO.	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.
MM		IEEE Draft Standard for "Scalable Coherent Interface-Low-Voltage Differential Signal Specifications And Packet Encoding", IEEE Standards Department, P1596.3/D0.15 (March 1992)
MM		IEEE Draft Standard for "High-Bandwidth Memory Interface Based on SCI Signaling Technology (RamLink)", IEEE Standards Department, Draft 1.25 IEEE P1596.4-199X (May 1995)
MM		IBM, "The PowerPC Architecture: A Specification For A New Family of Risc Processors", 2nd Ed., Morgan Kaufmann Publishers, Inc., (1994).
MM		Hewlett-Packard Co., "PA-RISC 1.1 Architecture and Instruction Set", Manual Part No. 09740-90039, (1990).
MM		MIPS Computer Systems, Inc., "MIPS R4000 User's Manual", Mfg. Part No. M8-00040, (1990).

EXAMINER

DATE CONSIDERED

*Blackly Hanestime*

*11/27/04*

\*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

1 Applicant's unique citation designation number (optional). 2 Applicant is to place a check mark here if English language Translation is attached.

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				APPLICANT <b>HANSEN, et al.</b>				
				FILING DATE <b>July 10, 2003</b>		GROUP <b>2483-2676</b>		
<b>U.S. PATENT DOCUMENTS</b>								
EXAMINER'S INITIALS	PATENT NO.	DATE	NAME	CLASS	SUBCLASS	FILING DATE		
MM	4,025,772	05/24/77	Constant					
	4,489,393	12/18/84	Kawahara, et al.					
	4,701,875	10/20/87	Konishi, et al.					
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						Yes	No	
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	Organization of the Motorola 88110 Superscalar RISC Microprocessor, Keith Diefendorff and Michael Allen.							
MM	Microprocessor Report, Volume 7 Number 13, October 4, 1993, IBM Regains Performance Lead with Power2, Six Way Superscalar CPU in MCM Achieves 126 SPECint92.							
	IBM Creates PowerPC Processors for AS/400, Two New CPU's Implement 64-Bit Power PC with Extensions by Linley Gwennap, July 31, 1995.							
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MM	0 474 246 A2	06/09/91	Japan				
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	Osborne McGraw-Hill, i860™ Microprocessor Architecture, Neal Margulis, Foreword by Les Kohn						
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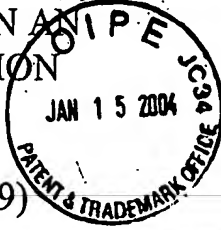
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	5,721,892	02/24/98	Peleg, et al.				
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